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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: T. Lakshmi Viswanathan, et al. Art Unit: 2816

Serial No.: 10/749,275 Examiner: Jeffery S. Zweizig

Filed: 12/29/03 Docket: TI-36954

For: LOAD SENSING VOLTAGE REGULATOR FOR PLL/DLL ARCHITECTURES

Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Applicant

T. Lakshmi Viswanathan, et al.

Docket Number: TI-36954

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X FACSIMILE COVER SHEET NEW APPLICATION DECLARATION ASSIGNMENT FORMAL DRAWINGS X INFORMAL DRAWINGS – 1 page CONTINUATION APP'N DIVISIONAL APP'N		AMENDMENT 111 (9 pages) EOT NOTICE OF APPEAL APPEAL (# Pages) ISSUE FEE & PUBLICATION FEE (1 Page) REPLY BRIEF (IN TRIPLICATE) (# Pages) ELECTION
NAME OF INVENTOR(S): T. Lakshmi Viswanathan, et al. TITLE OF INVENTION: Load Sensing Voltage Regulator for PLL/DLL Architectures		RECEIPT DATE & SERIAL NO.: Serial No.: 10/749,275 Filling Date: 12/29/03
TI-36954	DEPOSIT ACCT. NO.: 20-0668	
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